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501.32049RV1/6/29/

IN THE LINITED STATES PATENT AND TRADEMARK OFFICE?

Applicant: Yujiro KAJIHARA et al.

Serial No.: 09/987,978 (Rule 53b Divisional of 09/328,910 filed 9 June 1999)

Filed: 16 November 2001

For: LEAD FRAME SEMICONDUCTOR INTEGRATED

CIRCUIT DEVICE, USING THE SAME, AND METHOD OF AND PROCESS FOR FABRICATING THE TWO

Art Unit: Unassigned (Parent - 2815)

Examiner: Unassigned (Parent - J. Clark)

CLARIFYING PRELIMINARY AMENDMENT

Honorable Commissioner for Patents Washington, D.C. 20231

3 December 2001

Sir:

In order to clarify a typographical error inadvertently presented in the Preliminary Amendment filed 16 November 2001 with the above-identified application, the following is respectfully requested.

IN THE CLAIMS:

Please cancel original Claims 1-14, without prejudice or disclaimer.

Please enter Claims 15-27 as set forth in the Preliminary Amendment filed

16 November 2001 with the subject application.

REMARKS

This Clarifying Preliminary Amendment corrects an obvious clerical error present in the Preliminary Amendment filed with the application on 16 November 2001.

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